

VHDL

Checks

Check ID	Check Name	Supported
AC_00	No Control Code Characters	Yes
AC_01	No Direct or Indirect Recursion	Yes
AC_HIS_04	Cyclomatic Complexity (v(G))	Yes
AC_HIS_05	Calling Functions (CALLING)	Yes
AC_HIS_06	Called Functions (CALLS)	Yes
AC_HIS_07	Function Parameters (PARAM)	Yes
AC_HIS_09	Number of call levels (LEVEL)	Yes
AC_HIS_11	Language scope (VOCF)	Yes
AC_HIS_12	Recursion (AP_CG_CYCLE)	Yes
AC_HIS_13	Statements Changed (SCHG)	Yes
AC_HIS_14	Statements Deleted (SDEL)	Yes
AC_HIS_15	New Statements (SNEW)	Yes
AC_HIS_16	Stability Index (S)	Yes
HIS_01	1. Comment Density (COMF)	Yes
HIS_03	3. Number of Goto Statements(GOTO)	Yes
HIS_04	4. Cyclomatic Complexity (v(G))	Yes
HIS_05	5. Calling Functions (CALLING)	Yes
HIS_06	6. Called Functions (CALLS)	Yes
HIS_07	7. Function Parameters (PARAM)	Yes
HIS_09	9. Number of call levels (LEVEL)	Yes
HIS_11	11. Language scope (VOCF)	Yes
HIS_12	12. Recursion (AP_CG_CYCLE)	Yes
HIS_13	13. Statements Changed (SCHG)	Yes
HIS_14	14. Statements Deleted (SDEL)	Yes
HIS_15	15. New Statements (SNEW)	Yes
HIS_16	16. Stability Index (S)	Yes
METRIC_00	Program Unit Call Count	Yes
METRIC_01	Program Unit Callby Count	Yes
METRIC_02	Program Unit Comment to Code Ratio	Yes
METRIC_03	Program Unit Cyclomatic Complexity	Yes
METRIC_04	Program Unit Max Length	Yes
METRIC_05	Program Unit Max Nesting Depth	Yes
METRIC_06	Program Unit Parameters Count	Yes
METRIC_09	Coupling Between Object Classes	Yes
METRIC_13	Maintainability Index	Yes
RECOMMENDED_02	Files too long	Yes

Check ID	Check Name	Supported
RECOMMENDED_04	Functions Too Long	Yes
RECOMMENDED_06	Goto Statements	Yes
RECOMMENDED_10	Overly Complex Functions	Yes
RECOMMENDED_12	Unreachable Code	Yes
RECOMMENDED_13	Unused Functions	Yes
RECOMMENDED_18	Unused Local Variables	Yes
RECOMMENDED_19	Comments Indicating Future Fixes	Yes
RECOMMENDED_20	Duplicate Code	Yes
VHDL_1	VHDL_1 Unused declarations	Yes
VHDL_2	VHDL_2 Unconnected input ports	Yes
VHDL_3	VHDL_3 Unconnected output ports	Yes
VHDL_4	VHDL_4 Incomplete sensitivity lists shall not contain signals not needed by process	No
VHDL_5	VHDL_5 Processes and for loops without labels	Yes
VHDL_6	VHDL_6 Inference of latches	Yes
VHDL_7	VHDL_7 Multiple drivers	Yes
VHDL_8	VHDL_8 Hard coded numeric values	Yes
VHDL_9	VHDL_9 Duplicate signal assignments	Yes
VHDL_10	VHDL_10 Signals not initialized before use	Yes
VHDL_11	VHDL_11 Nested case statements	Yes
VHDL_12	VHDL_12 Commented out code	Yes
VHDL_13	VHDL_13 Cyclomatic Complexity	Yes
VHDL_14	VHDL_14 Functions shall not have more than 8 input parameters	Yes
VHDL_15	VHDL_15 Source code line length shall not exceed 150 characters	Yes
VHDL_16	VHDL_16 Tabs shall not be used for indentation	Yes
VHDL_17	VHDL_17 Files shall not have more than 4500 executable statements	Yes
VHDL_18	VHDL_18 There shall not be multiple statements on a single line	Yes
VHDL_19	VHDL_19 Else and Elself statements shall be on their own lines	Yes
VHDL_20	VHDL_20 Nested if statements	Yes
VHDL_21	VHDL_21 Duplicate statements in a case statement	Yes
VHDL_22	VHDL_22 Incomplete case statements	Yes
VHDL_23	VHDL_23 Unreachable case items	No
VHDL_24	VHDL_24 Do not use While loop	Yes
VHDL_25	VHDL_25 Do not use Loop without For	Yes
VHDL_26	VHDL_26 Do not use Wait	Yes

Check ID	Check Name	Supported
VHDL_27	VHDL_27 Do not use Delay	Yes
VHDL_28	VHDL_28 Unlabeled Processes	Yes
VHDL_N001	Naming Convention: Architectures	Yes
VHDL_N002	Naming Convention: Configurations	Yes
VHDL_N003	Naming Convention: Entities	Yes
VHDL_N004	Naming Convention: Files	Yes
VHDL_N005	Naming Convention: Packages	Yes
VHDL_N006	Naming Convention: Processes	Yes
VHDL_N007	Naming Convention: Subtypes	Yes
VHDL_N008	Naming Convention: Types	Yes
VHDL_N009	Naming Convention: Functions	Yes
VHDL_N010	Naming Convention: Signals	Yes